

S/N 10/022,776

CLAIM ELECTION UNDER 37 CFR 1.143

Applicants elect, without traverse, Group I, consisting of claims 1 – 8, 12,
5 14 and 16, inclusive.

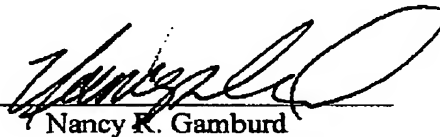
REMARKS:

10 On the basis of the above claim election, the application is in condition for
examination, and an early action toward that end is respectfully solicited. In addition, for
any issues or concerns, the Examiner is invited to call the attorney for the applicant at the
telephone number provided below.

15 Respectfully submitted,
Paul L. Master, et al.

20 January 17, 2007

By

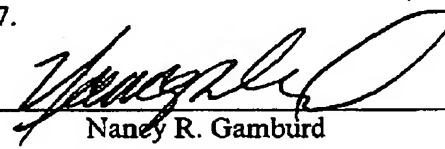


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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that the foregoing Claim Election in Response to
5 Restriction Requirement (3 pages), Transmittal (PTO/SB/21) (1 page), Fee Transmittal
(PTO/SB/17) (1 page), Petition for Extension of Time (PTO/SB/22) (2 pages, original
plus one copy) (7 pages total), for Master, Paul L. et al., U.S. Patent Application Serial
No. 10/022,776, entitled "Computer Processor Architecture Selectively Using Finite-
State Machine for Control Code Execution", have been have been transmitted by
10 facsimile to the US Patent and Trademark Office to fax number (571) 273-8300
(Centralized Facsimile Number), on January 17, 2007.


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